

Electronic Filing System (EFS) Data

Electronic Patent Application Submission

USPTO Use Only

EFS ID: 19421

Application ID: 10065589

Title of Invention: METHOD OF PREPARING WHOLE SEMICONDUCTOR WAFER FOR ANALYSIS

First Named Inventor: Allan Paterson

Domestic/Foreign Application: Domestic Application

Filing Date: null

Effective Receipt Date: 2002-10-31

Submission Type: Utility Patent Filing

Filing Type: new-utility

Confirmation Number: 0

Attorney Docket Number: 2969.004

Digital Certificate Holder: cn=Thomas F. Lebens, ou=Registered Attorneys, ou=Patent and Trademark Office, ou=Department of Commerce, o=U.S. Government, c=US

Certificate Message Digest: ksRuMltp2ywOedze3JF/5A==

Total Fees Authorized: \$446.0

Payment Category: DA - Deposit Account

Deposit Account Number: 501616

Deposit Account Name: Thomas F. Lebens



Figure 1 consists of two schematic diagrams. Diagram (a) shows a subject's head and shoulders in profile, looking at a screen. On the screen, there is a starting position (a small circle) and a target (a larger circle). A horizontal line connects the starting position to the target. Diagram (b) shows the same subject, but their hand is extended towards the target. The hand is represented by a simple line drawing of a hand and forearm. The target is still visible on the screen.

10/065589
10/31/02

2969.004

Submission Type: Utility Patent Filing

First Named Inventor: Mr. Allan Paterson

Name:	Mr. Thomas F. Lebens
Registration Number:	38221
Electronic Signature Mark: /tfl/	Date Signed: 20021031

I, the undersigned, certify that I have viewed a display of document(s) being electronically submitted to the United States Patent and Trademark Office, using either the USPTO provided style sheet or software, and that this is the document(s) I intend for initiation or further prosecution of a patent application noted in the submission. This document(s) will become part of the official electronic record at the USPTO.

declaration	POA1.tif
declaration	POA2.tif
declaration	Declaration.tif


Comments:

Please type a plus sign (+) inside this box → ☐

Approved for use through 10/31/2002. CMB 0451-0035
U.S. Patent and Trademark Office, U.S. DEPARTMENT OF COMMERCE
Under the Paperwork Reduction Act of 1995, no persons are required to respond to a collection of information unless it displays a valid OMB control number

POWER OF ATTORNEY OR AUTHORIZATION OF AGENT	Application Number	
	Filing Date	
	First Named Inventor	Allan Paterson
	Title	METHOD OF PREPARING WHOLE SEMICONDUCTOR WAFER FOR ANALYSIS
	Group Art Unit	
	Examiner Name	
	Attorney Docket Number	2969.004

I hereby appoint:

☒ Practitioners at Customer Number → 

OR

☐ Practitioner(s) named below:


Name	Registration Number

as my/our attorney(s) or agent(s) to prosecute the application identified above, and to transact all business in the United States Patent and Trademark Office connected therewith

Please change the correspondence address for the above-identified application to:

☒ The above-mentioned Customer Number.

OR

☐ Practitioners at Customer Number → 

OR

☐ Firm or Individual Name

SINSHEIMER, SCHIEBELHUT & BAGGETT

Address

Address

City State Zip

Country

Telephone Fax

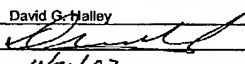
I am the:

☒ Applicant/Inventor

☐ Assignee of record of the entire interest. See 37 CFR 3.71.
Statement under 37 CFR 3.73(b) is enclosed. (Form PTO/SB/96).

SIGNATURE of Applicant or Assignee of Record

Name

Signature 

Date


NOTE: Signatures of all the inventors or assignees of record of the entire interest or their representative(s) are required. Submit multiple forms if more than one signature is required, see below.

☒ *Total of forms are submitted.

Burden Hour Statement: This form is estimated to take 3 minutes to complete. Time will vary depending upon the needs of the individual case. Any comments on the amount of time you are required to complete this form should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, Washington, DC 20231. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Assistant Commissioner for Patents, Washington, DC 20231.

Title of Invention	METHOD OF PREPARING WHOLE SEMICONDUCTOR WAFER FOR ANALYSIS
<p>As the below named inventor(s), I/we declare that.</p> <p>This declaration is directed to.</p> <p><input checked="" type="checkbox"/> The attached application, or</p> <p><input type="checkbox"/> Application No. _____, filed on _____</p> <p><input type="checkbox"/> as amended on _____ (if applicable),</p> <p>I/we believe that I/we am/are the original and first inventor(s) of the subject matter which is claimed and for which a patent is sought;</p> <p>I/we have reviewed and understand the contents of the above-identified application, including the claims, as amended by any amendment specifically referred to above;</p> <p>I/we acknowledge the duty to disclose to the United States Patent and Trademark Office all information known to me/us to be material to patentability as defined in 37 CFR 1.56, including for continuation-in-part applications, material information which become available between the filing date of the prior application and the national or PCT International filing date of the continuation-in-part application.</p> <p>All statements made herein of my/own knowledge are true, all statements made herein on information and belief are believed to be true, and further that these statements were made with the knowledge that willful false statements and the like are punishable by fine or imprisonment, or both, under 18 U.S.C. 1001, and may jeopardize the validity of the application or any patent issuing thereon.</p>	

Inventor one: Allan Paterson
Signature: *allan* Citizen of: US

Inventor two: David G. Halley
Signature:  Citizen of: US

Inventor three: _____

Signature: _____ Citizen of: _____

Inventor four: _____

Signature: _____ Citizen of: _____

☐ Additional inventors are being named on _____ additional form(s) attached hereto.

Burden Hour statement. This collection of information is required by 35 U.S.C. 115 and 37 CFR 1.63. The information is used by the public to file (and the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This form is estimated to take 1 minute to complete. This time will vary depending upon the needs of the individual case. Any comments on the amount of time you are required to complete this form should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, Washington, DC 20231. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Assistant Commissioner for Patents, Washington, DC 20231

FEE TRANSMITTAL

Electronic Version 1.1.0

Stylesheet Version: 1.0

Patent fees are subject to annual revisions on or about October 1st of each year.

Small Entity

Small Business Concern

TOTAL FEES AUTHORIZED: \$ 446

The commissioner is hereby authorized to charge indicated processing and/or publication fees and credit any overpayments to:

Deposit Account Number: 50-1616



Deposit Account Name: Sinsheimer, Schiebelhut & Baggett

Charge Any Additional Fee Required Under 37 C.F.R. Sections 1.16 and 1.17.

Charge Assignment Fees Required Under 37 C.F.R. Section 1.21 (h).

SUBMITTED BY

Authorized Name: Thomas F. Lebens

Electronic Signature Mark: /tfl/

Date Signed: 20021031

BASIC FILING FEE

Fee Description	Fee Code	Fee Paid
Utility Filing Fee	2001	\$ 370

Subtotal For Basic Filing Fee: \$ 370

EXTRA CLAIM FEES

	Fee Code	Fee	Extra Claims	Fee Paid
Total Claims: 24	2202	\$ 9	4	\$ 36
Independent Claims: 2	2201	\$ 42	0	\$ 0

Subtotal For Extra Claims Fees: \$ 36

ADDITIONAL FEES

Fee Description	Number	Quantity	Fee Code	Amount	Fee Paid
Recording Each Patent Assignment Per Property Fee	00000000	1	8021	\$ 40	\$ 40

Subtotal For Additional Fees: \$ 40